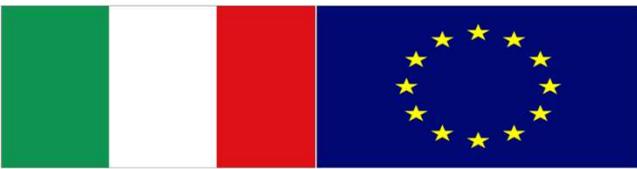


Chips JU 2026 Information Day
Roma, February 23rd, 2026

Chips Mirror Group Italy

2026

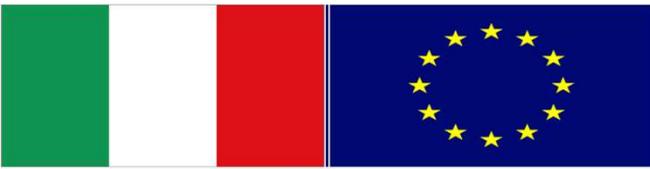
Livio Baldi



What is it?



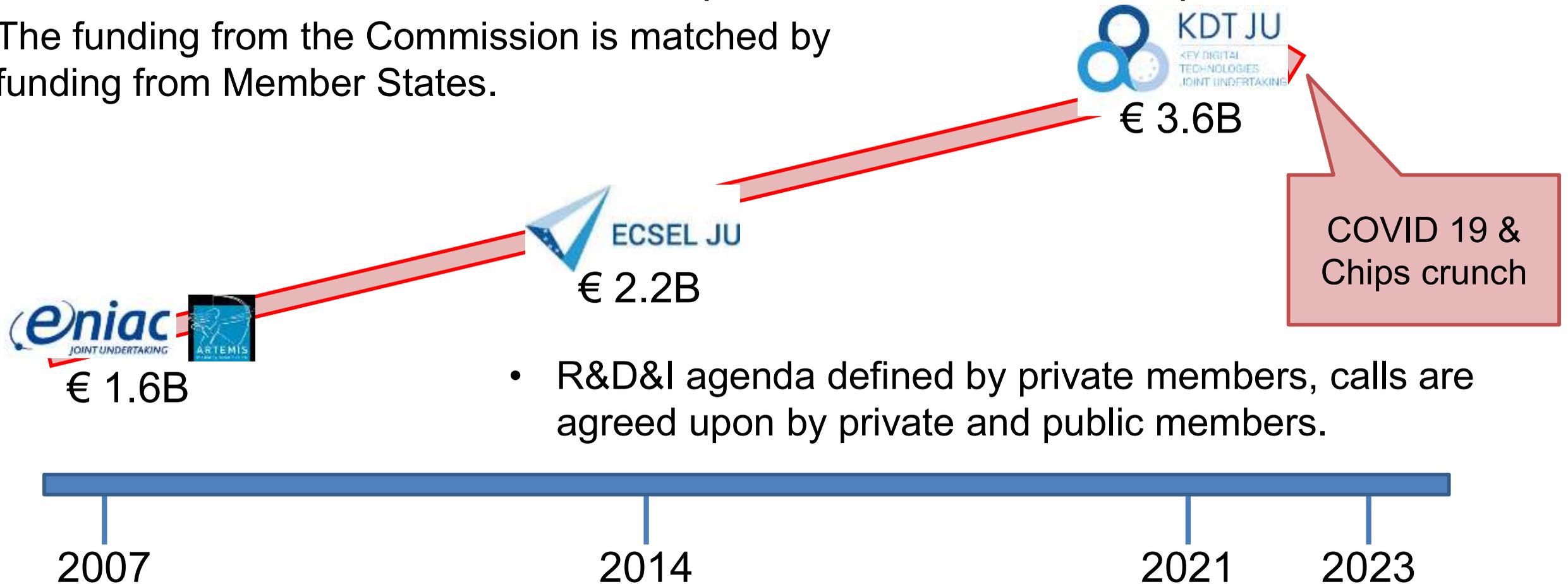
- The Chips Mirror Group Italy was founded in 2015 as ECSEL Italy, and adapted its name to the new Joint Technology Initiatives on semiconductors.
- It includes Italian Industrial and academic representatives playing a role in the Industrial organizations supporting the JTI's and involved in its governance.
- **Its mission**
- To facilitate and coordinate the Italian participation to the calls of the Joint Technology Initiatives on semiconductors.
- Chips Mirror Group Italy has the administrative support of AEIT and Fondazione Chips-IT



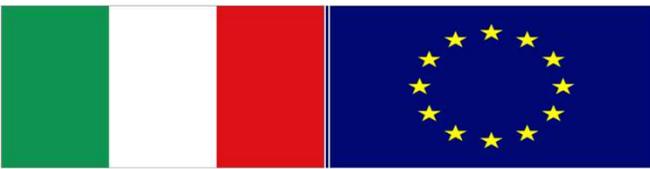
The Joint Undertakings (JTI)



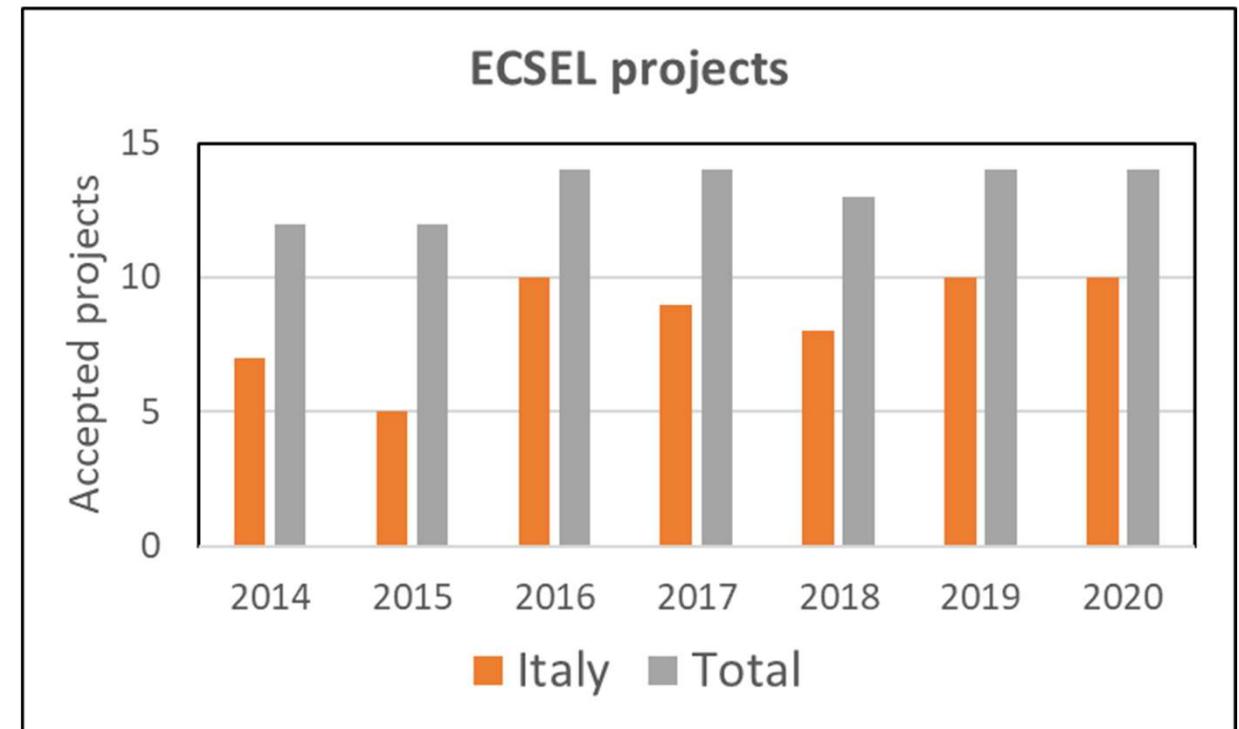
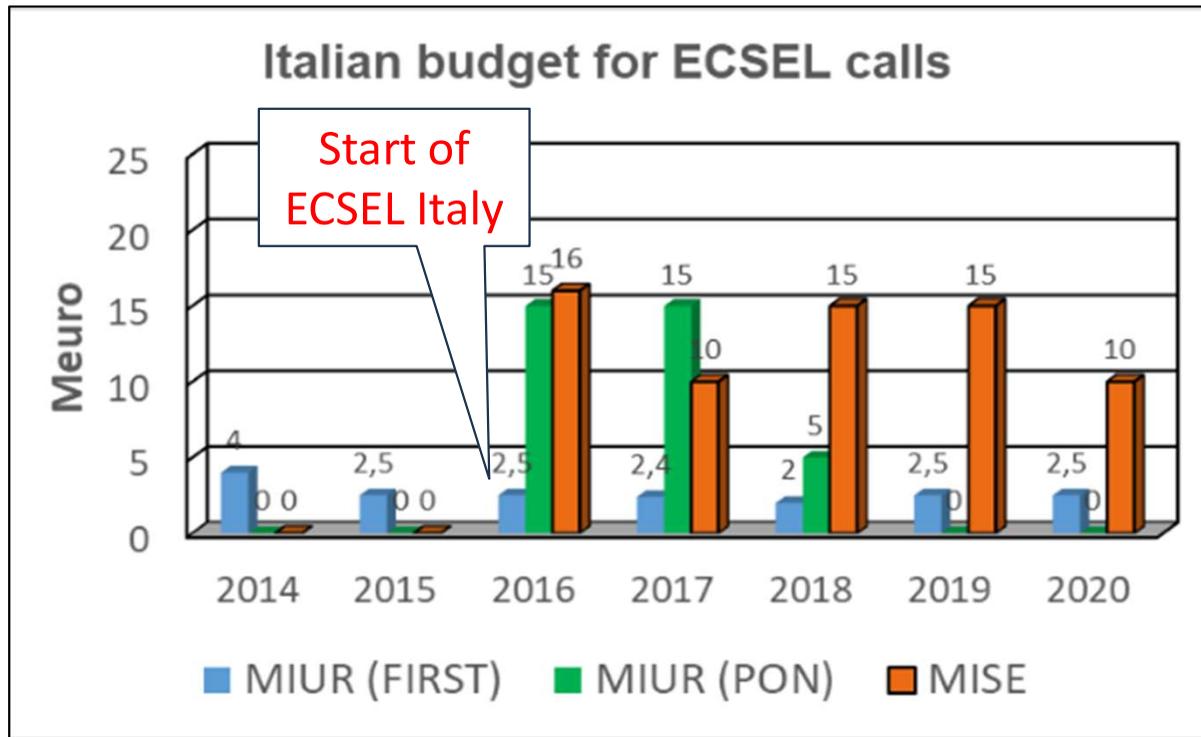
- JU's (or JTI'S; Joint Technology Initiatives) are part of the Public-Private Partnerships (PPP): **joint R&D&I initiatives among Commission, Member States and industrial/academic associations (AENEAS, INSIDE, EPOSS).**
- The funding from the Commission is matched by funding from Member States.



- R&D&I agenda defined by private members, calls are agreed upon by private and public members.

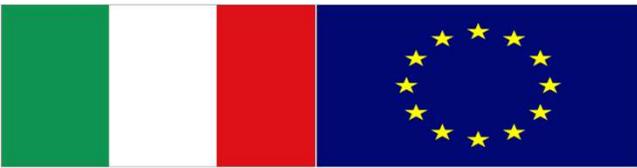


Italian participation (2014-2020)



In the period 2014-2020:

- there have been 279 Italian participations by 139 partners
- there were Italian partners in 59 research projects over 93 launched
- among the top 5 Italian participants, 4 are Universities and RTO



2022: European Chips Act



European Semiconductor Board (Governance)

Pillar 1

Chips for Europe Initiative

- Initiative on infrastructure building in synergy with the EU's research programmes
- Support to start-ups and SMEs

Pillar 2

Security of Supply

- First-of-a-kind semiconductor production facilities

Attract new fabs in Europe

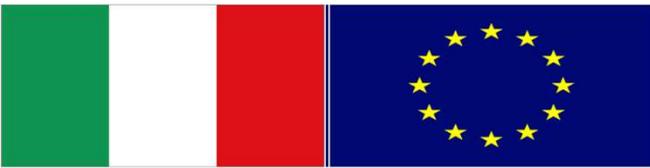
Pillar 3

Monitoring and Crisis Response

- Monitoring and alerting
- Crisis coordination mechanism with MS
- Strong Commission powers in times of crisis

Chips JU:

- Calls
- Pilot Lines
- Competence Centers



Chips for Europe

To promote European role

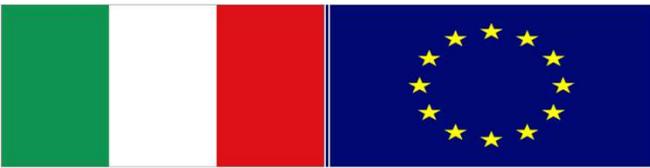
- Pilot Lines
- Competence Centres
- Design Platform



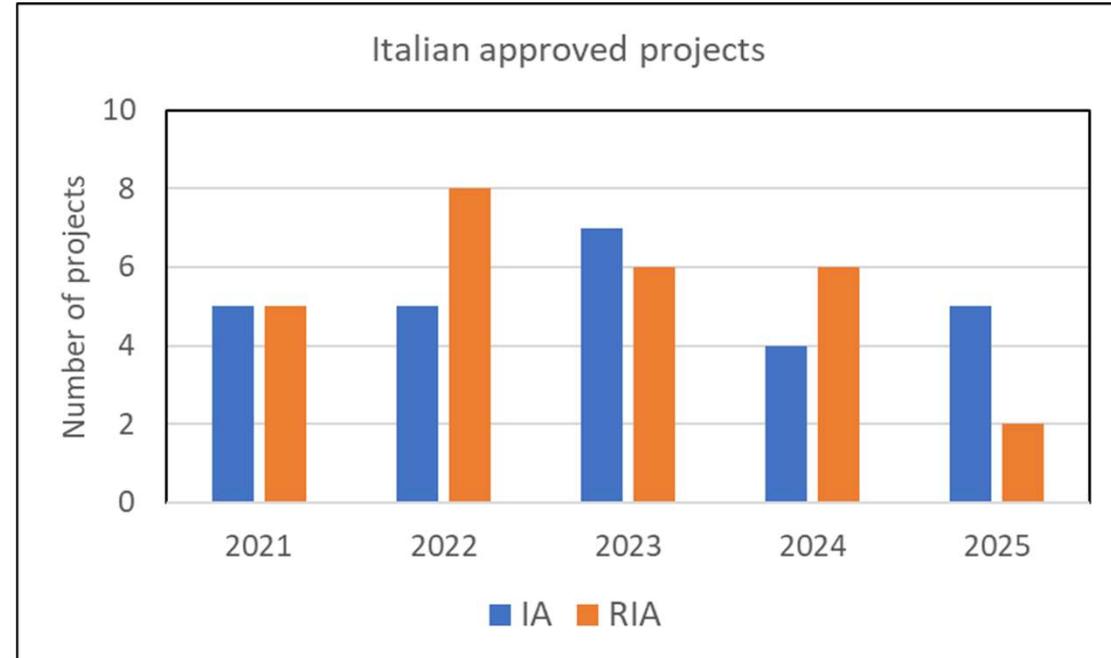
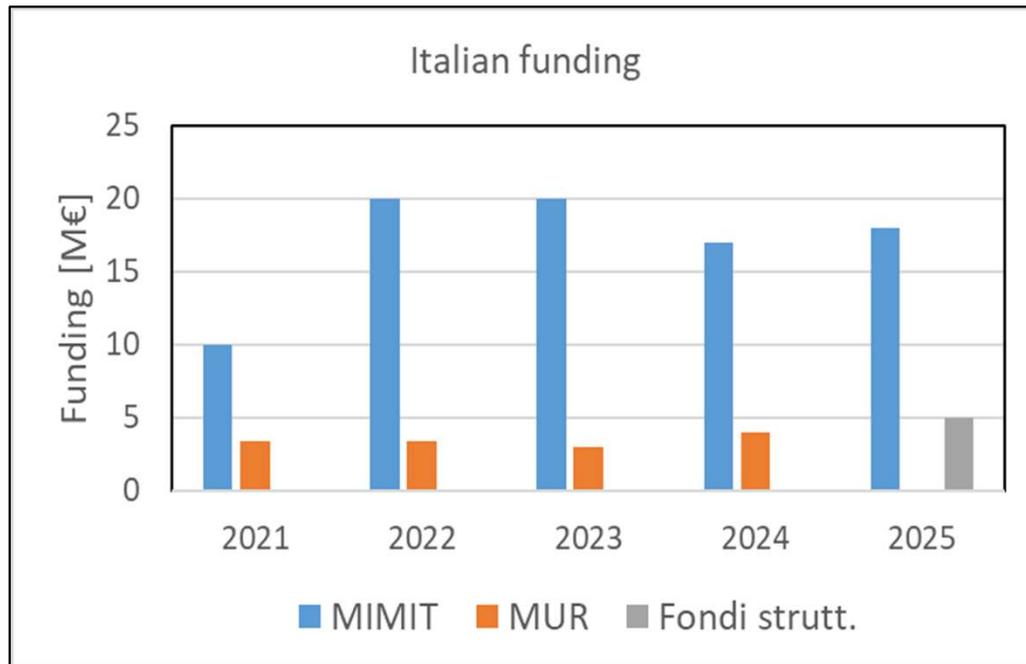
ECS R&I Calls

Follow-up of KDT JU with yearly calls:

- open on SRIA
- focus topics

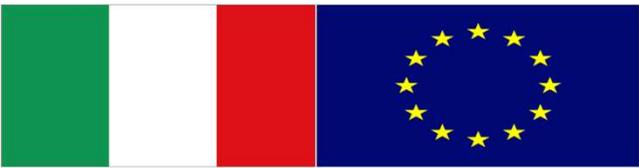


Italian participation (2021-2024)

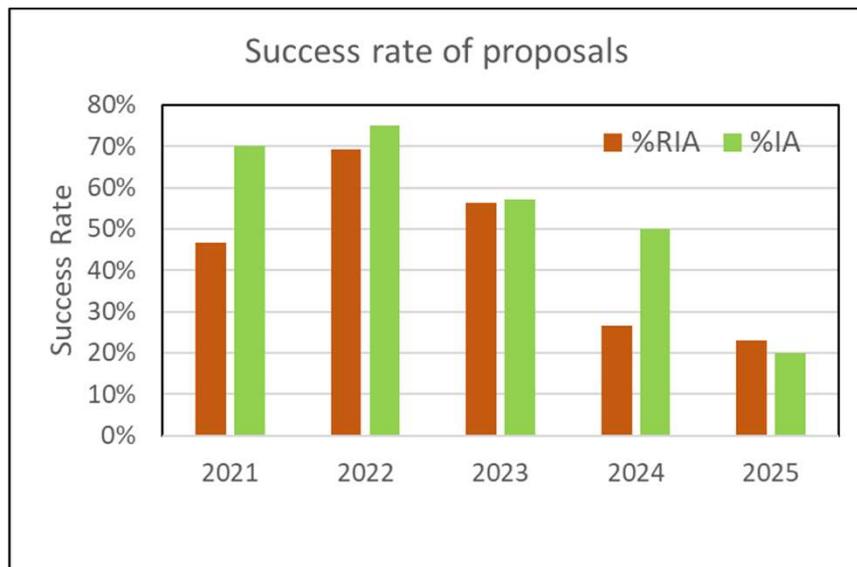
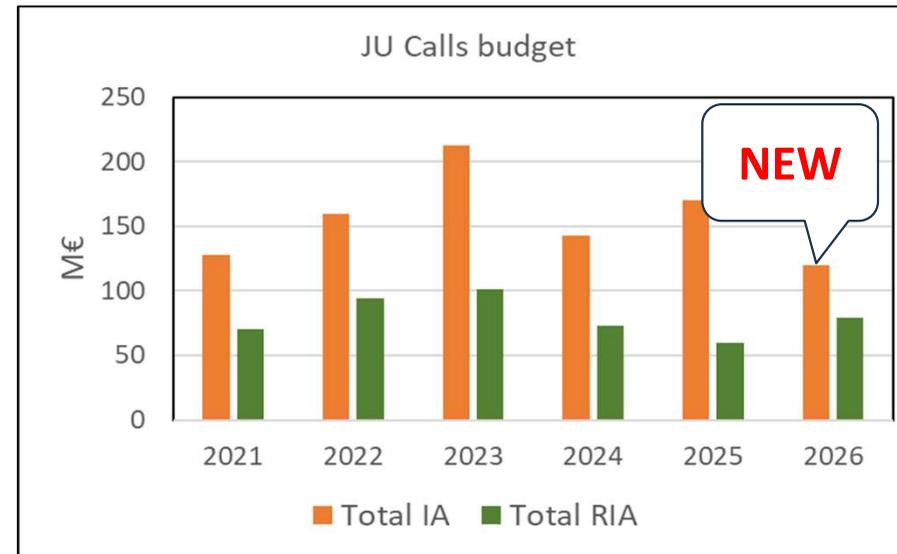
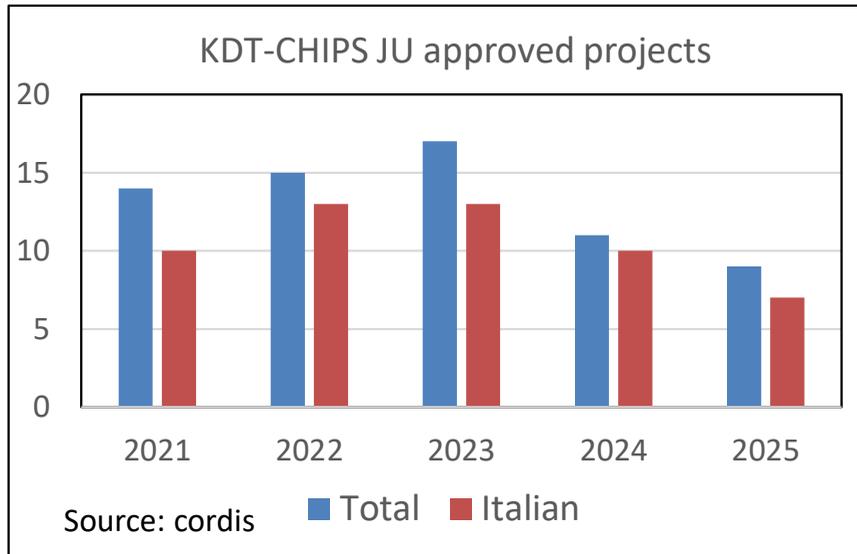


In the period 2021-2024:

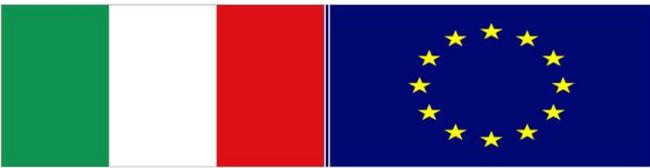
- MUR has approved 23 proposals for 15,4 M€
- MIMIT has approved 17 proposals (until 2023), for 12,7M€. Other 6 were under evaluation.
- Funding for JU calls has been kept available all through the period.
- Time to national funding is still an issue.



Budget and projects



- Italian partners have been present in $\approx 80\%$ of projects
- However, the **number** of funded projects has been continuously decreasing.
- Due to lower acceptance rates and budget utilization .

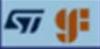
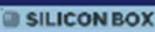


Chips Act Pillar 2

First of a kind manufacturing lines

Planned investments by major manufacturers exceeding **EUR 80 Billion**

The Commission *approved* state aid for the following projects so far:

Company	MS	Location	Total Investment (B€)		Technology
 ST Microelectronics	IT	Catania	0.73		SiC wafer
 ST Micro & GlobalFoundries	FR	Crolles	7.5		300-mm FD-SOI
 ST Microelectronics	IT	Catania	5		SiC devices
 ESMC (J.V. TSMC+Bosch/IFX/NXP)	DE	Dresden	>10		CMOS, FinFET
 Silicon Box	IT	Novara	3.2		Advanced packaging
 Infineon	DE	Dresden	4.46		Discrete, Analog/mixed signals
 ams Osram	AT	Premstätten	0.567		Optoelectronic sensors
 ephos	IT	Milan	0.1		Photonic chips



Other Italian Initiatives



Fondazione Chips-IT

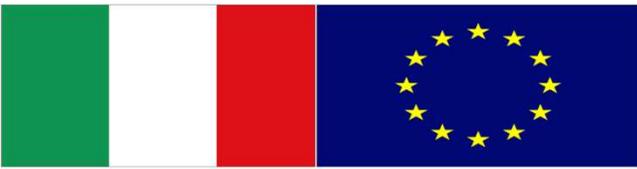
Il Centro italiano per il design dei circuiti integrati a semiconduttori, svilupperà attività di ricerca e design in collaborazione con attori pubblici e privati, mettendo a disposizione attrezzature e software di ultima generazione.



Contratti di sviluppo – Nuovo sportello Semiconduttori



3,3 Miliardi di euro di contributi a fondo perduto e finanziamento agevolato per sostenere la crescita e lo sviluppo tecnologico della catena di approvvigionamento dei semiconduttori

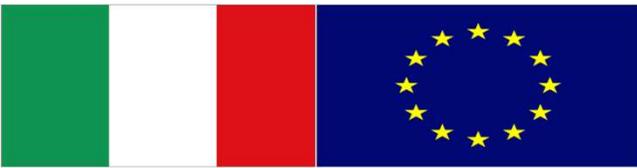


What can we do for you?



- Disseminate information on Chips JU events and calls.
- Coordinate with Public Authorities to support Italian projects.
- Coordinate with Industrial Associations in Italy and Europe to define research program (SRIA) and support funding to projects.
- Collect project ideas and presentation of partners' competences for future brokerage events.
- Provide advice and support in the search for projects partners and in proposal preparation.

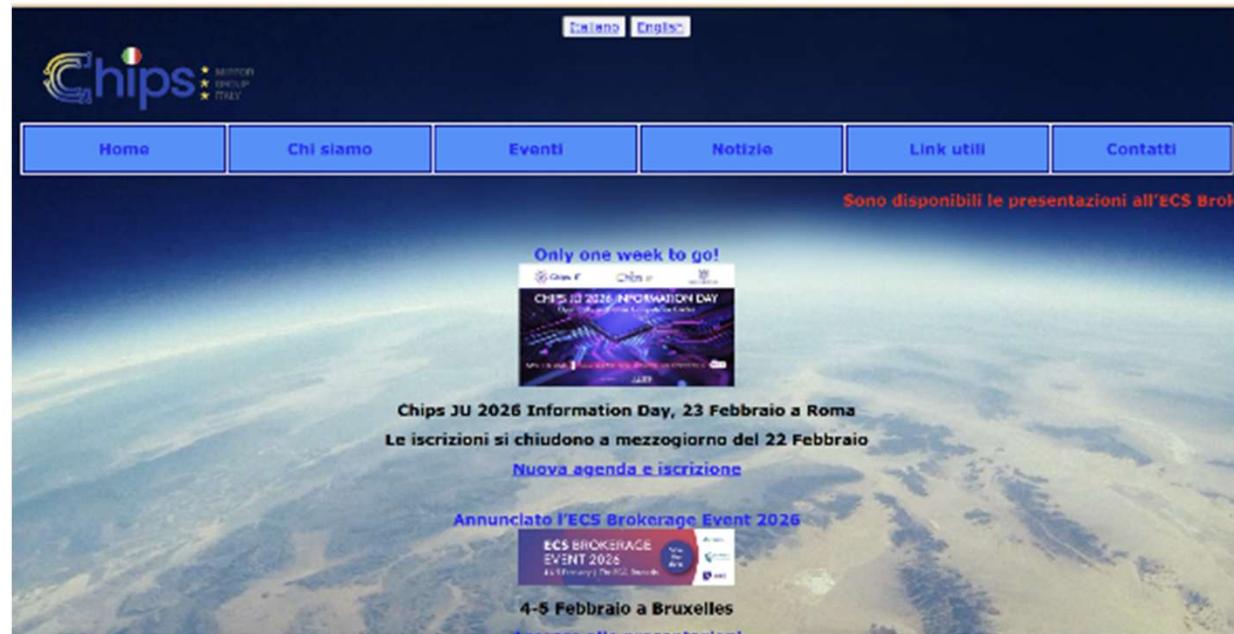


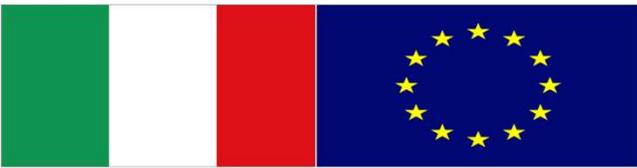


Follow us on the web sites



A web site is available at:
<https://www.chips-mgi.it/>





Thank you!

<http://www.aeit.it/ecsel>

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- roberto.zafalon@st.com (Vice-chairman Industry)

Steering Committee

